PE0601

Evaluation Kit for the CMX7x6x

The PE0601-7x6x Platform Evaluation Kit is designed to assist in the evaluation and application development of the CMX7x6x range of FirmASIC™ products.

Features

- On-board Access to all CMX7x6x Signals, Commands and Data
- CMX7x6x FirmASIC Product Range Evaluation
- Serial Flash Option for Function Image with In-circuit Programming
- On-board Supply Regulators Operate from a Single 5-Volt Supply
- Instrumentation Interface to Differential I and Q Signals
- Command and Control by PC via the PE0003 Universal Interface Card or User’s µC Development Application or Emulator
- 19.2MHz Oscillator, User Xtal or External Clock Input to CMX7x6x
- Interfaces to RF Daughterboard with all Necessary Signals and Levels

Supply Requirement

- +/-5 V

For further information, please refer to the ‘Design Resources’ section on the CMX7x6x FirmASIC™ product pages at cmlmicro.com

The kit is in the form of a populated PCB comprising a CMX7x6x target IC and appropriate supporting components and circuitry.

The board also incorporates all of the necessary power-supply regulation facilities for operation from a single 5 volt supply, or a dual +/-5 volt supply if the instrumentation interface is required.

The board is fitted with a C-BUS connector allowing the operation via connection to either of the two C-BUS ports on a CML PE0003 Universal Interface Card, and used with the associated PC GUI software, or by direct connection between the CMX7x6x C-BUS and the user’s µC development application or emulation system.

The CMX7x6x Function Image (FI) can be loaded, on power-up, directly into the on-board target IC using the PE0003 interface or the user’s system. Alternatively, it can be automatically loaded from the on-board serial memory, on power-up.

In this case, the on-board serial memory has to be pre-loaded with the FI by using the ‘Program Serial Memory’ tab on the PE0003 GUI software, or by the user’s system utilising the appropriate ‘thick stub’ programme. All of this software is available from the CML website.

Function images suitable for the CMX7x6x range of products can be downloaded from the CML Technical Portal.
CML Microcircuits Benefits

**Faster time to market**
Developing proven high performance and field tested ASSP ICs, CML is helping engineers to cope with increasing pressure in delivering shorter project design cycles.

**Design flexibility**
CML’s FirmASIC® reconfigurable technology with the use of a Function Image upload enables a single hardware platform to be used for multiple communications systems.

**High Quality**
With 100% of products being tested before shipping, customers are assured of the highest reliability.

**Product Longevity**
Designing with CML products, manufacturers are rewarded with longer product life cycles and a stable BOM, ensuring minimum engineering costs and maximum profit.

**Low Power**
Being at the forefront of low power chip technology, manufacturers can develop smaller equipment with extended battery life.

**Superior Support**
Internal and field based applications teams worldwide provide focused customer support to ease the development process.