

# FX929B

## 4FSK RD-LAP Packet Data Modem

The FX929B is a CMOS integrated circuit containing all of the baseband signal processing and Medium Access Control (MAC) protocol required for a high performance 4FSK Wireless Packet Data Modem. It interfaces with the modem host processor and the radio modulation/demodulation circuits, delivering reliable two-way transfer of application data over wireless link.

The FX929B assembles application data received from the processor, adds forward error correction (FEC) and error detection (CRC) information and interleaves the result for burst-error protection. The modem converts the packet into filtered 4-level analogue signals for modulating the radio transmitter. In receive mode, the FX929B performs the reverse function using analogue signals from the receiver discriminator.

Any residual uncorrected errors in the data will be flagged and a readout of the SNR value during receipt of a packet is also provided.

### Features

- 4FSK modulation
- RD-LAP compatible
- Half duplex, 4.8kb/s to 19.2kb/s operation
- Full data packet framing
- Flexible operation modes host processor interface

### Applications

- RD-LAP systems
- DataTAC terminals

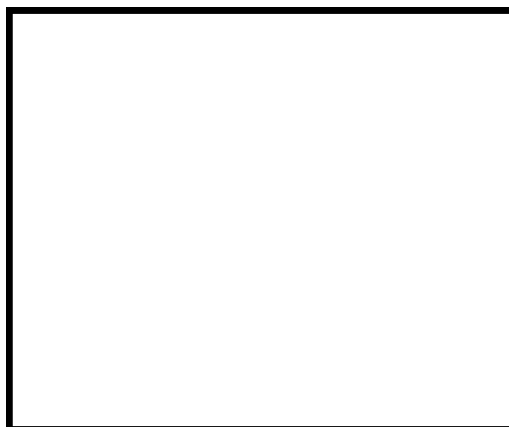
### Supply Requirement

- 3.0V—5.5V

The device uses data block sizes and FEC/CRC algorithms compatible with the RD-LAP over-air standard.

The format used is suitable for other private applications that require high-speed transfer of data over narrow-band wireless links.

The device is programmable to operate at most standard bit-rates from a wide choice of Xtal/clock frequencies.



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## WHAT TO DO NEXT

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## CML Microcircuits Benefits

### Faster time to market

Developing proven high performance and field tested ASSP ICs, CML is helping engineers to cope with increasing pressure in delivering shorter project design cycles.

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CML's *FirmASIC*<sup>®</sup> reconfigurable technology with the use of a Function Image upload enables a single hardware platform to be used for multiple communications systems.

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